



Massachusetts
Institute of
Technology

Extremely cost-effective semiconductor layer-transfer process via graphene & Highly uniform advanced RRAM

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1. 2D material based layer transfer
2. Highly uniform epitaxial RRAM (epiRAM)



Jeehwan Kim
Research Group

<http://jeehwanlab.mit.edu>



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1. 2D material based layer transfer (2DLT)

Sponsors



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Research Group

<http://jeehwanlab.mit.edu>

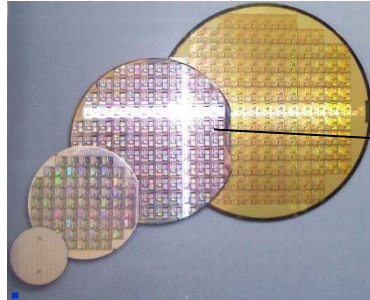
Major bottleneck for advancing semiconductor technology



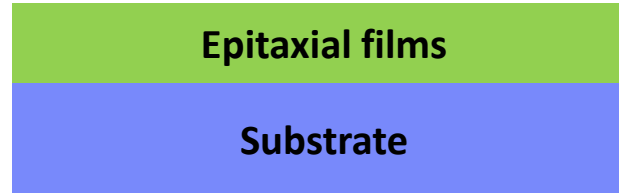
Substrate: Essential building block to form Electronic/optoelectronic devices

Epitaxial growth: Process for forming device film structures on the substrate

FETs, LEDs, Lasers, Detectors

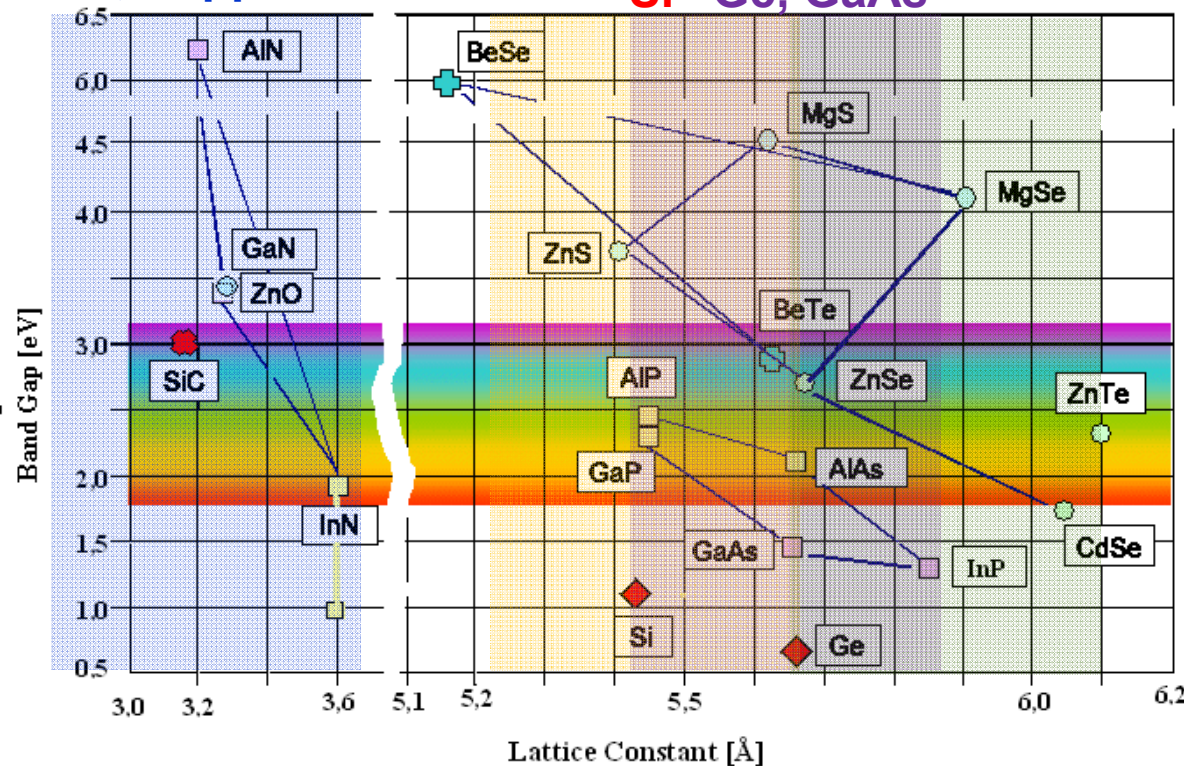


Epitaxy of single-crystalline films is required on given available substrates



SiC, Sapphire

Si Ge, GaAs InP



Price:
SiC > InP > GaAs > Ge >>> Si

Limited application

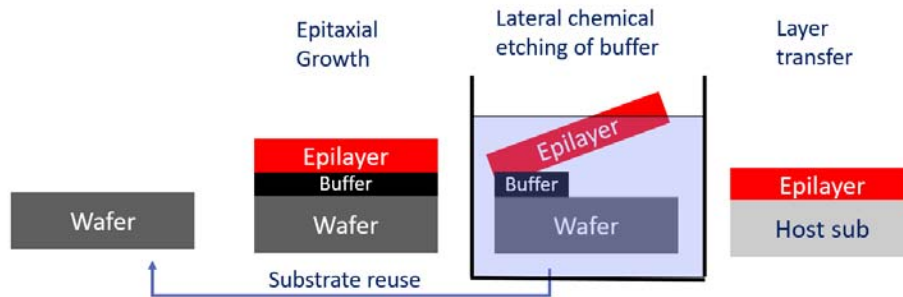
Lattice:
InP > GaAs/Ge > Si > SiC

Defect generation

Conventional lift-off technique

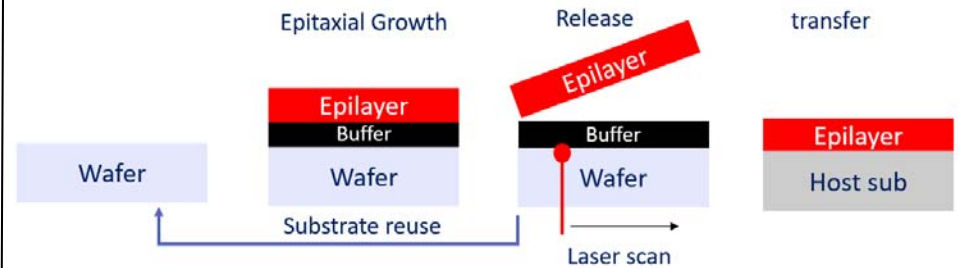


Chemical lift-off (epitaxial lift-off, ELO)



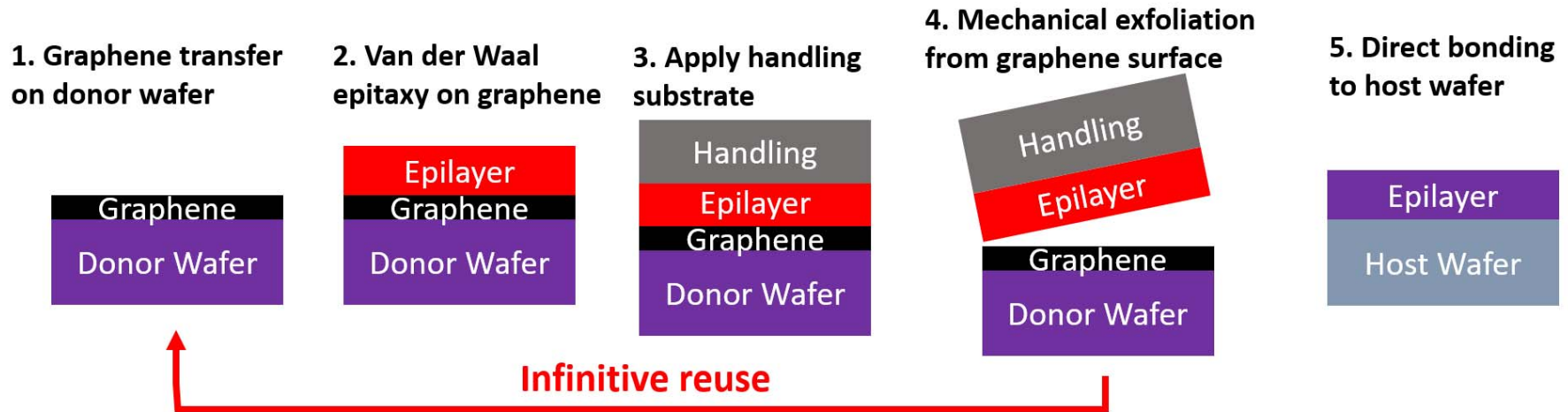
- **Pro: Control of release interface**
- **Cons:**
 - ❑ Post-treatment required
 - ❑ Slow release
 - ❑ Limited application mainly for GaAs & InP

Optical lift-off (Laser lift-off, LLO)



- **Pro: Control of release interface**
- **Cons:**
 - ❑ Post-treatment required
 - ❑ Cracking from local pressurization
 - ❑ Slow release
 - ❑ Limited application mainly for transparent substrate

2D material based layer transfer (2DLT)



■ sp^2 -bonded graphene: No broken bonds on the surface

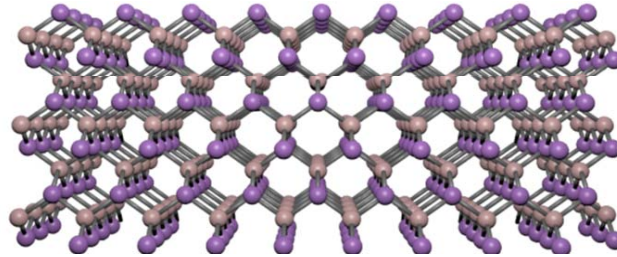
- Precise release from graphene
- Post-release treatment NOT required
- **1 sec** release due to weak interaction
- **Universal for any materials**

2DLT enabled by "REMOTE EPITAXY"

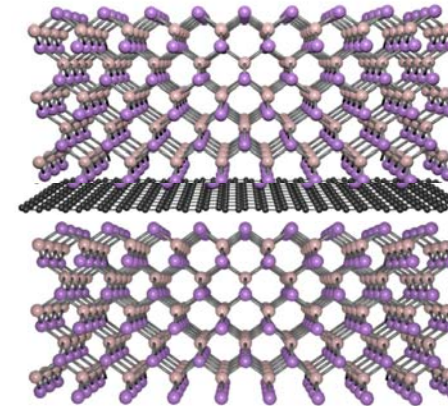
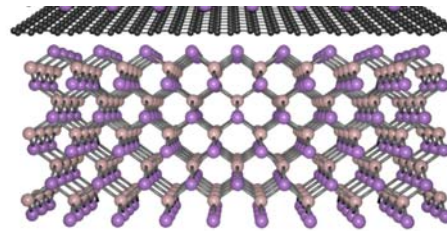
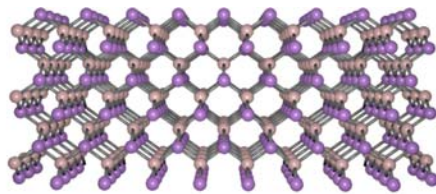


Conventional Epitaxy

Epitaxial Film
Substrate

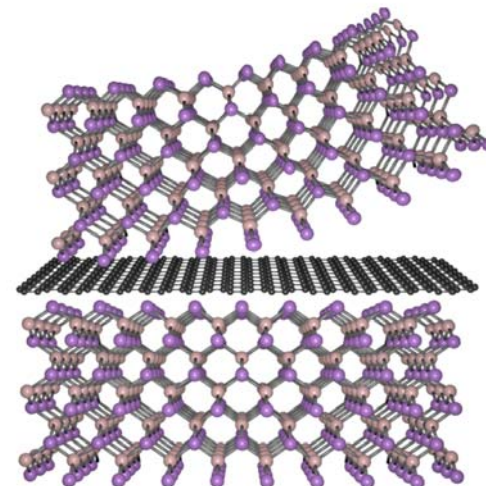


Remote Epitaxy

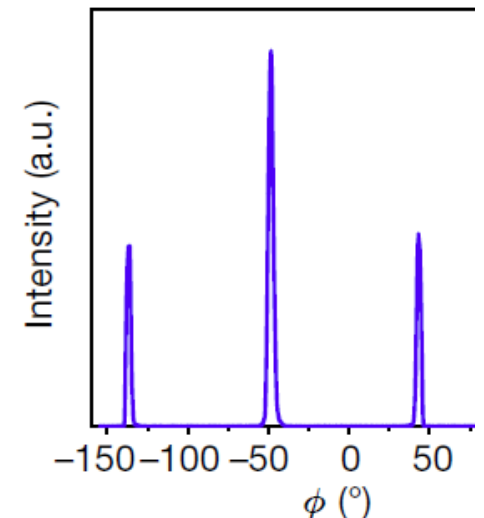
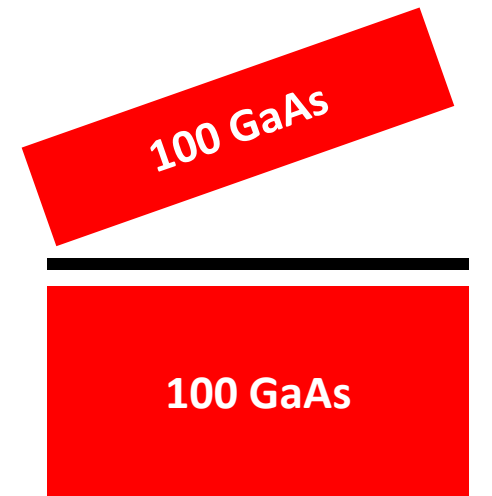
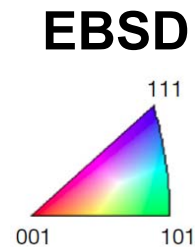
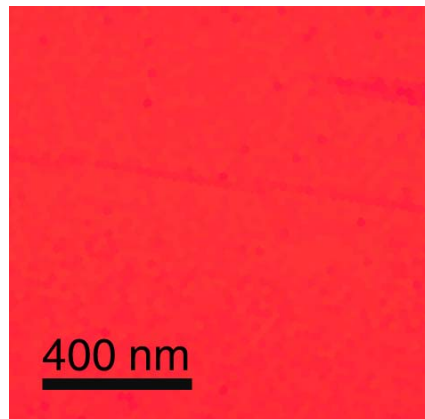
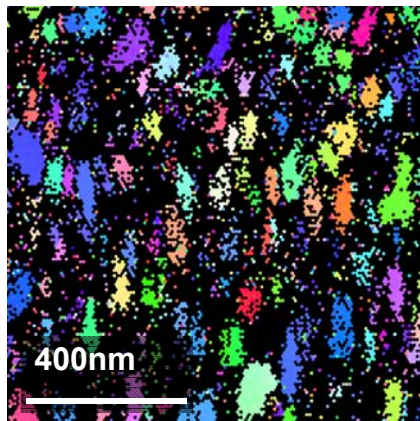
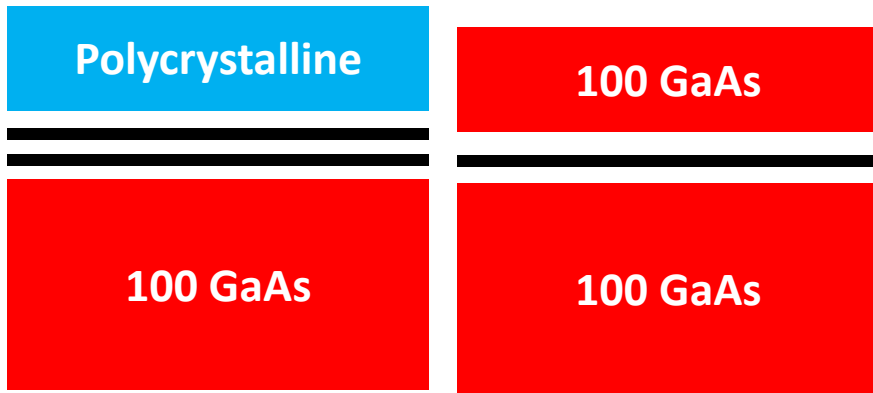


Epitaxial Layer
Graphene
Substrate

2DLT



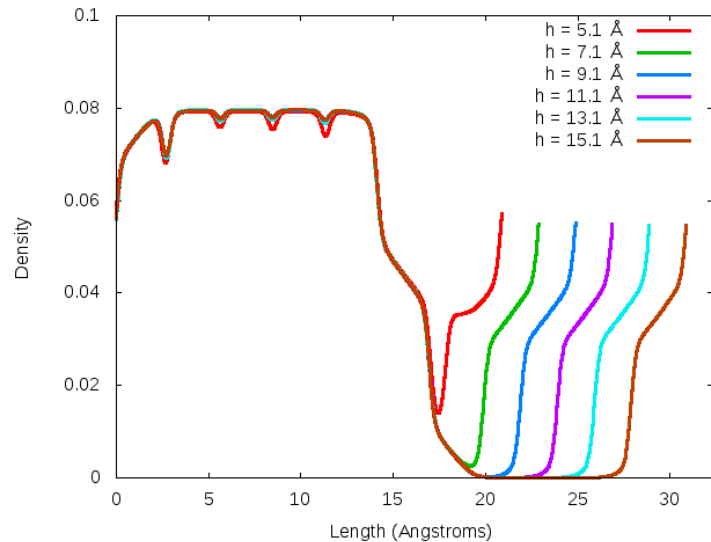
Remote epitaxy of GaAs(001) film on GaAs(001) substrate through “monolayer graphene”



Remote homoepitaxy: copy/paste dislocation-free films



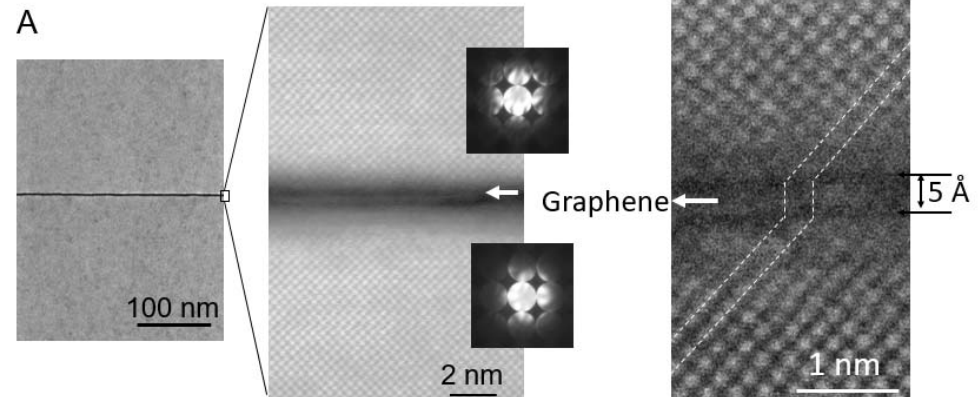
DFT calculation



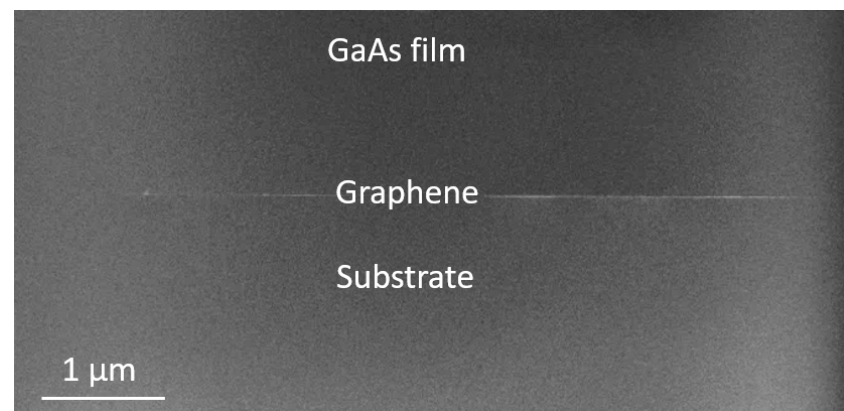
Critical interaction gap: 1 nm

In collaboration with Prof. Kolpak

HRTEM



Dark field XTEM: Strain field



No sign of dislocation

Remote homoepitaxy is possible through graphene

Y. Kim, S. Cruz, J. Kim et al., *Nature* (2017)

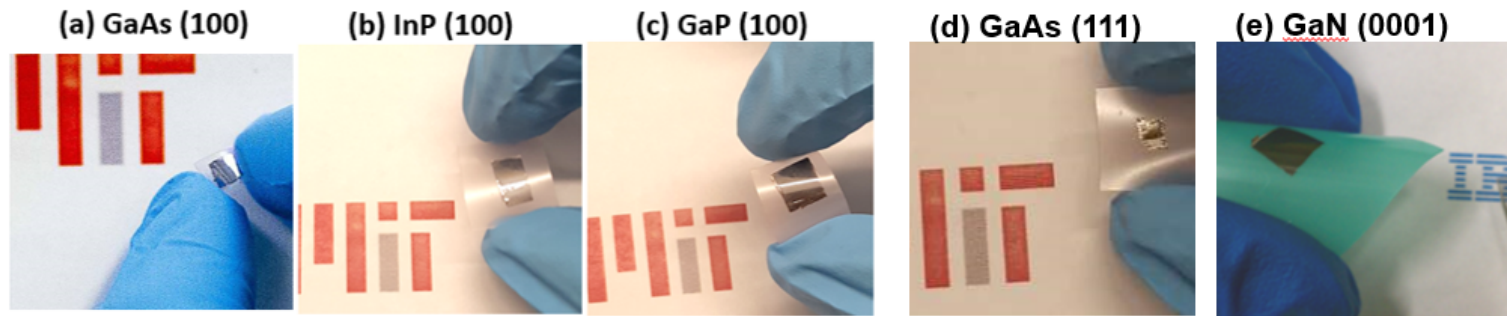
Universality of 2DLT



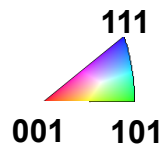
Growth of single-crystalline GaN, GaAs, InP, GaP, Ge on graphene

Periodic Table of the Elements

1 IA TA H Hydrogen (1.00794)	2 IIA ZA He Helium (4.002602)																	18 VIIIA BA He Helium (4.002602)					
3 Li Lithium (6.941)	4 Be Beryllium (9.012182)																	13 IIIA SA B Boron (10.811)	14 IVA 4A C Carbon (12.011)	15 VA 5A N Nitrogen (14.007)	16 VIA 6A O Oxygen (15.999)	17 VIIA 7A F Fluorine (18.998)	18 Ne Neon (20.180)
11 Na Sodium (22.990)	12 Mg Magnesium (24.305)																	31 Al Aluminum (26.982)	32 Si Silicon (28.086)	33 P Phosphorus (30.974)	34 S Sulfur (32.065)	35 Cl Chlorine (35.453)	36 Ar Argon (39.948)
19 K Potassium (39.098)	20 Ca Calcium (40.078)	21 Sc Scandium (44.956)	22 Ti Titanium (47.883)	23 V Vanadium (50.942)	24 Cr Chromium (51.996)	25 Mn Manganese (54.938)	26 Fe Iron (55.845)	27 Co Cobalt (58.933)	28 Ni Nickel (58.693)	29 Cu Copper (63.546)	30 Zn Zinc (65.38)	31 Ga Gallium (69.723)	32 Ge Germanium (72.631)	33 As Arsenic (74.922)	34 Se Selenium (78.96)	35 Br Bromine (79.904)	36 Kr Krypton (83.80)						
37 Rb Rubidium (85.468)	38 Sr Strontium (87.62)	39 Y Yttrium (88.906)	40 Zr Zirconium (91.224)	41 Nb Niobium (92.906)	42 Mo Molybdenum (95.94)	43 Tc Technetium (98)	44 Ru Ruthenium (101.07)	45 Rh Rhodium (102.91)	46 Pd Palladium (106.36)	47 Ag Silver (107.87)	48 Cd Cadmium (112.41)	49 In Indium (114.82)	50 Sn Tin (118.71)	51 Sb Antimony (121.76)	52 Te Tellurium (127.6)	53 I Iodine (126.91)	54 Xe Xenon (131.29)						
55 Cs Cesium (132.91)	56 Ba Barium (137.33)	57-71 Lanthanides	72 Hf Hafnium (178.49)	73 Ta Tantalum (180.95)	74 W Tungsten (183.85)	75 Re Rhenium (186.21)	76 Os Osmium (190.23)	77 Ir Iridium (192.22)	78 Pt Platinum (195.08)	79 Au Gold (196.97)	80 Hg Mercury (200.59)	81 Tl Thallium (204.38)	82 Pb Lead (207.2)	83 Bi Bismuth (208.98)	84 Po Polonium (209)	85 At Astatine (210)	86 Rn Radon (222)						
87 Fr Francium (223)	88 Ra Radium (226)	89-103 Actinides	104 Rf Rutherfordium (261)	105 Db Dubnium (262)	106 Sg Seaborgium (263)	107 Bh Bohrium (264)	108 Hs Hassium (265)	109 Mt Meitnerium (266)	110 Ds Darmstadtium (267)	111 Rg Roentgenium (268)	112 Cn Copernicium (269)	113 Uut Ununtrium (270)	114 Fl Flerovium (271)	115 Uup Ununpentium (272)	116 Lv Livermorium (273)	117 Uus Ununseptium (274)	118 Uuo Ununoctium (276)						



EBSD mapping



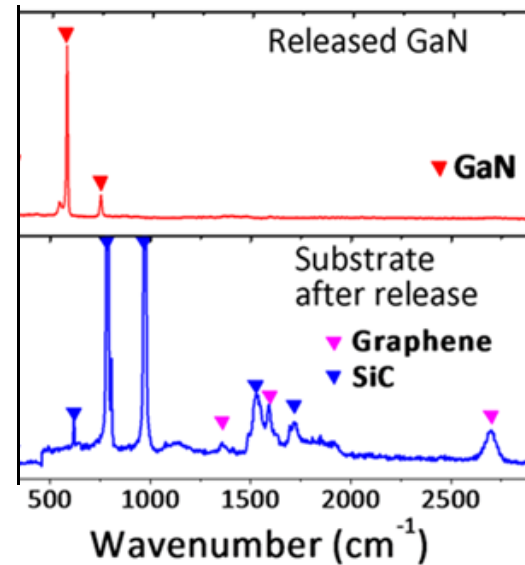
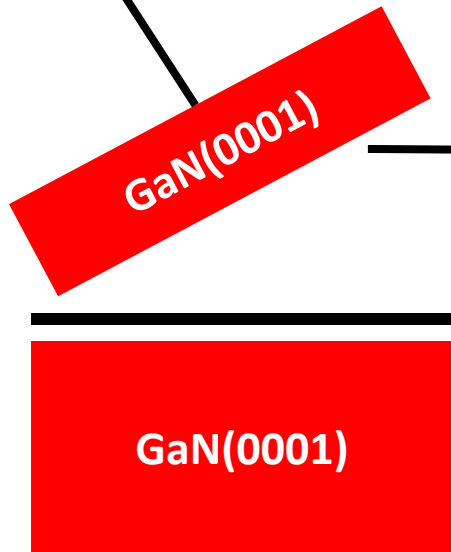
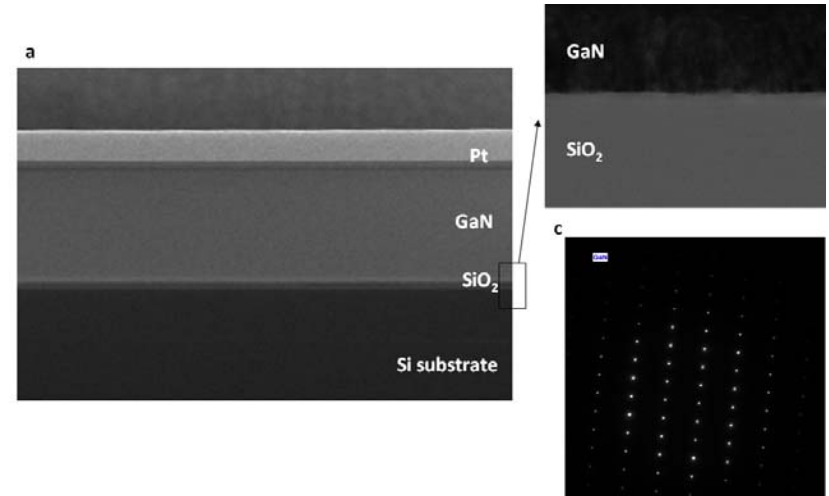
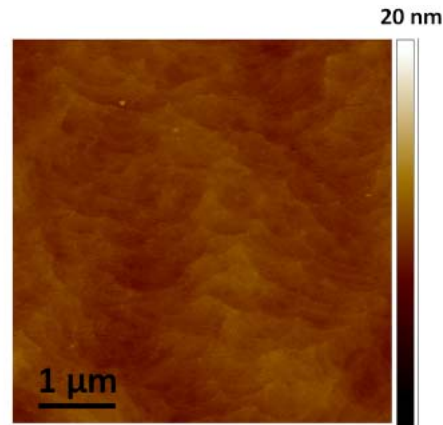
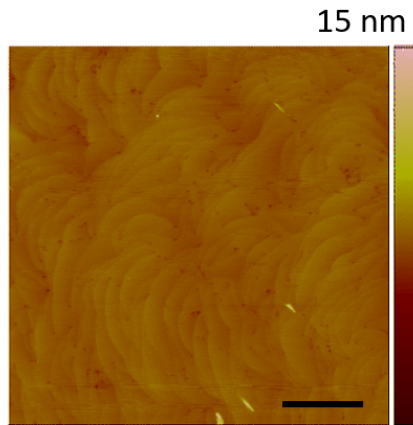
Graphene Reusability (GaN as an example)



Front Surface
(RMS roughness: 0.3 nm)

Released Surface
(RMS roughness: 0.3 nm)

Direct bondability



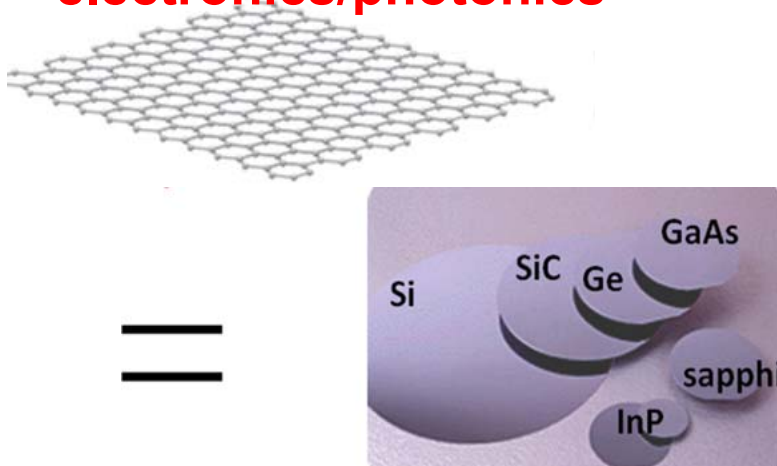
J. Kim et al.,
Nature Communications
Vol. 5, 4836 (2014)

No post-treatment required for further recycle

Role of graphene

- Turning wafers into the copy machine
- Dislocation-reducer/filter
- Release layer → 1sec release
- Wafer Surface protection → infinite reuse

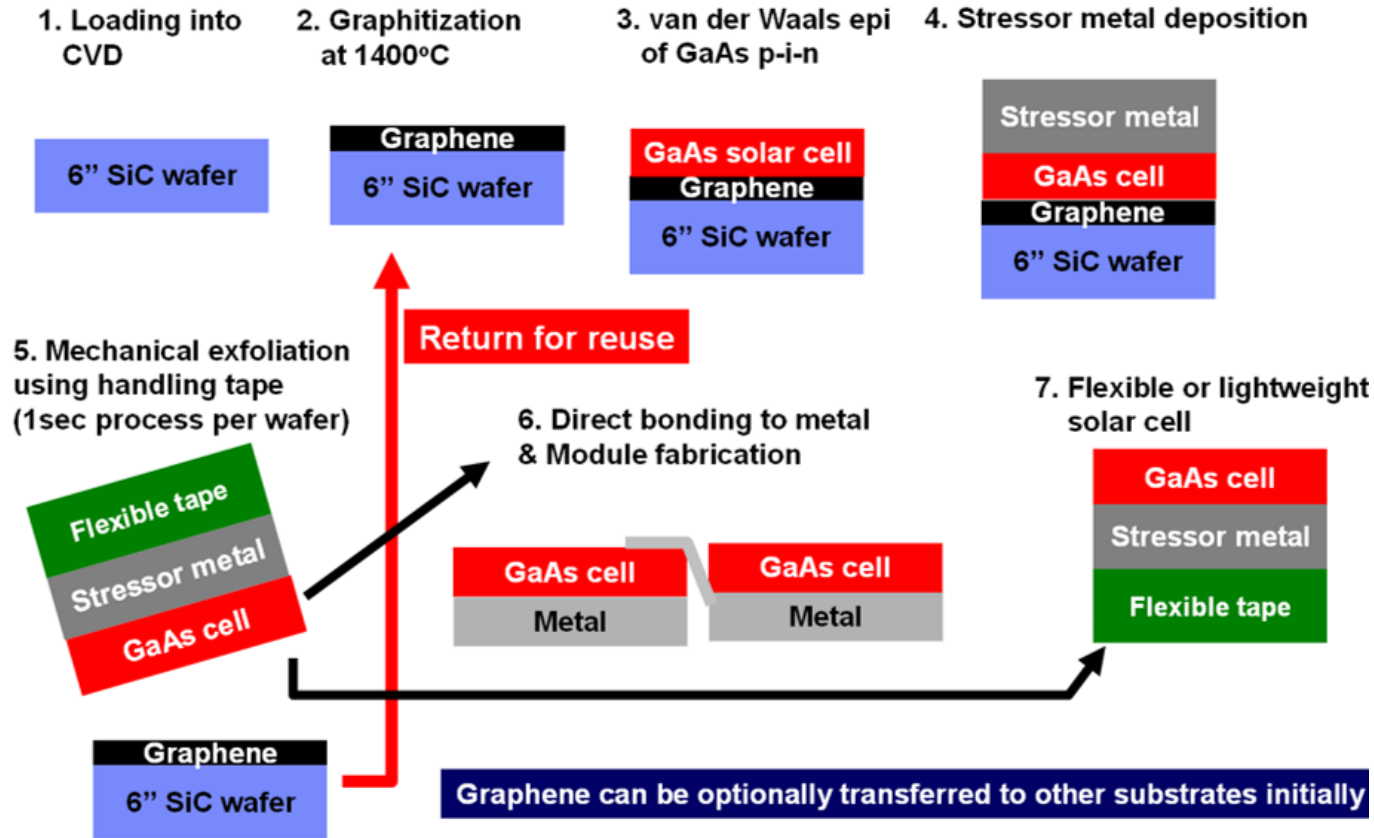
Wide application of non-Si electronics/photronics



Enabled heterointegration

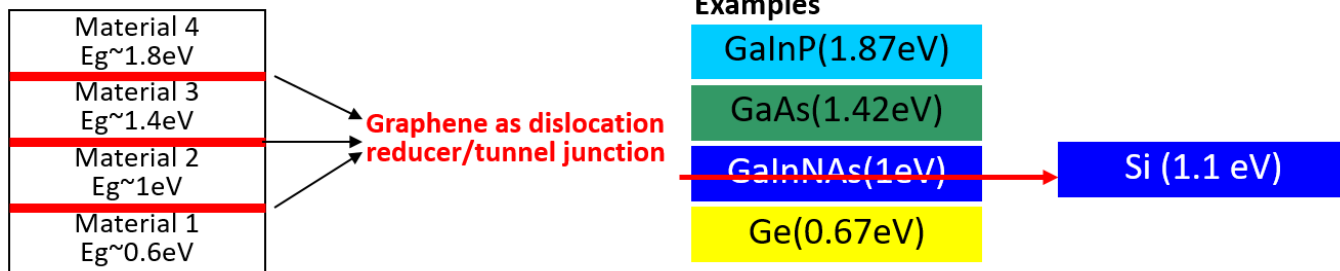


Implication for PV technology



Sponsor: LG

III-V multijunction solar cells for E_g oriented design



Sponsor: Masdar INSTITUTE

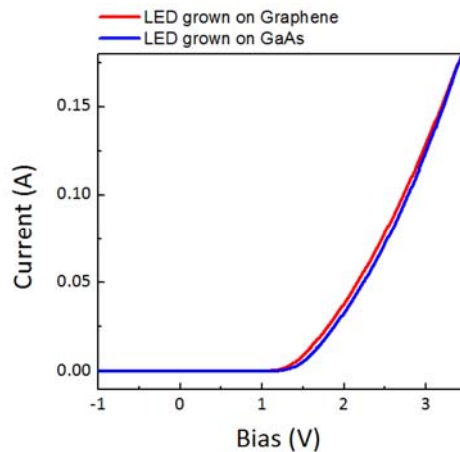
Low-cost flexible LEDs (solid state lighting/microLED)

Red LED (III-V)

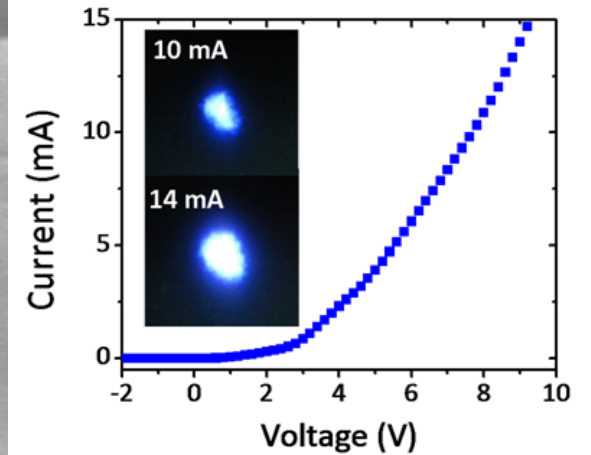
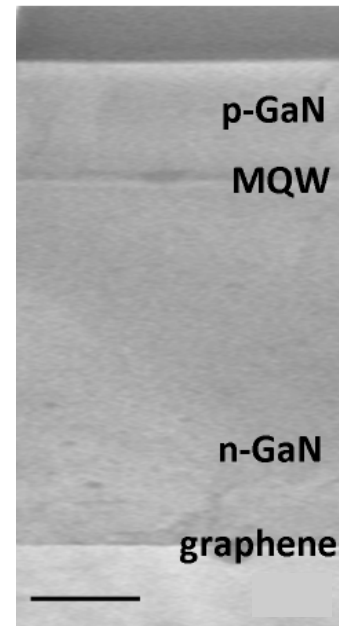
Dislocation-free LED on graphene



Light emission from LEDs on graphene

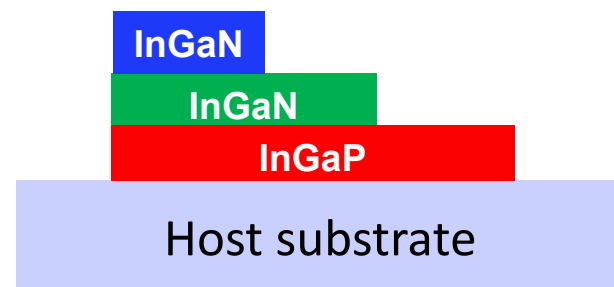


Blue LED (III-N)

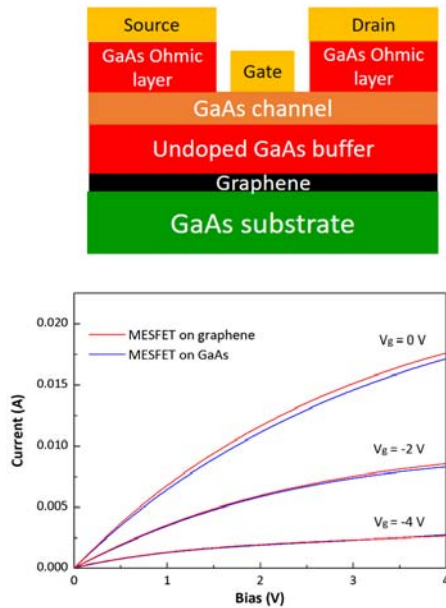


Dislocation-free GaN obtainable by GaN growth on graphene/GaN

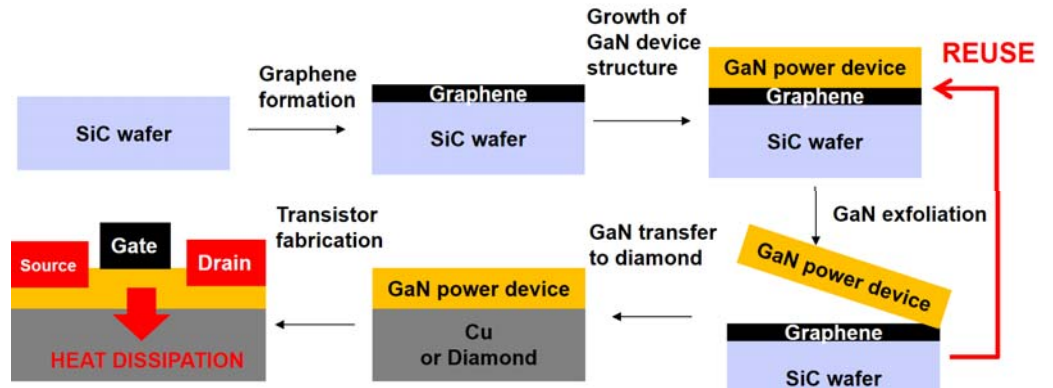
High efficiency lighting
High pixel density microLED



Implication for power electronics/heterointegration



Power electronics



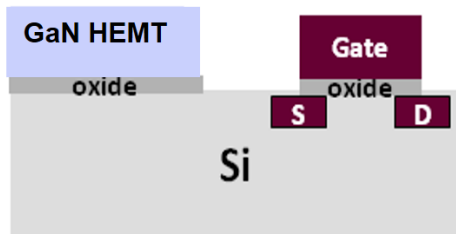
Sponsor: LINCOLN LABORATORY
MASSACHUSETTS INSTITUTE OF TECHNOLOGY

Thanks to gift from:

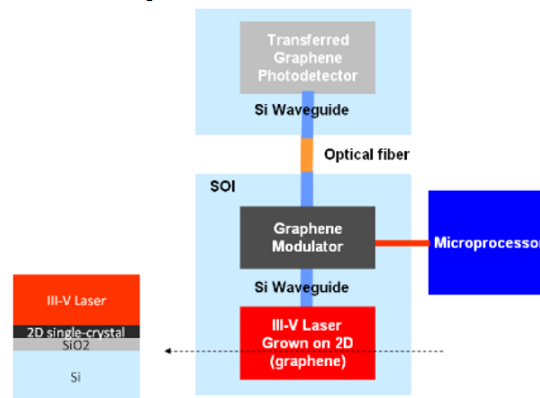


Heterointegration

Power transistor



optical interconnect



Wide field of view focal plane arrays





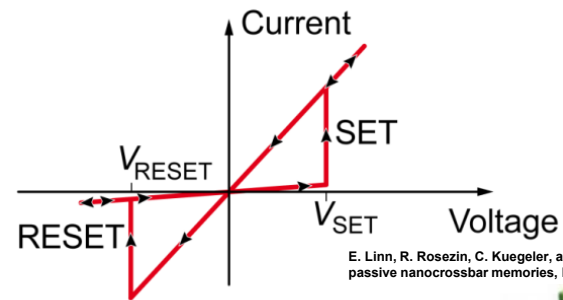
2. Highly uniform advanced RRAM

- Epitaxial RAM (epiRAM)

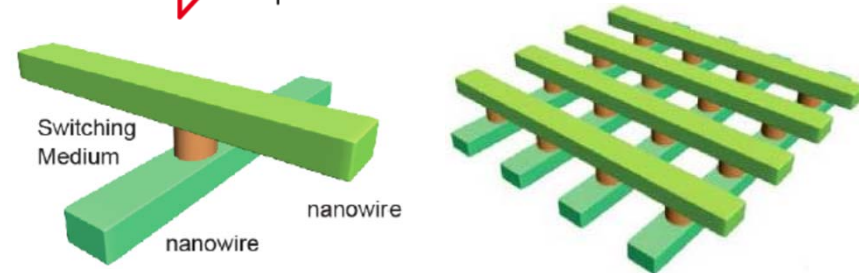


Why Resistive Random Access Memory (RRAM)?

- Wide applications
 - Neuromorphic computing, NVM storage, Logic
- High scalability (10nm size)
- <ns switching
- Large connectivity (2-terminal structure)
- Low energy consumption
- 3D structure
- CMOS compatibility



E. Linn, R. Rosezin, C. Kuegeler, and R. Waser, Complementary resistive switches for passive nanocrossbar memories, *Nat. Mater.* 9, 403-406 (2010)



S. H. Jo, et al., *Nano Lett.*, 9, 2009.

■ Requirement for commercialization

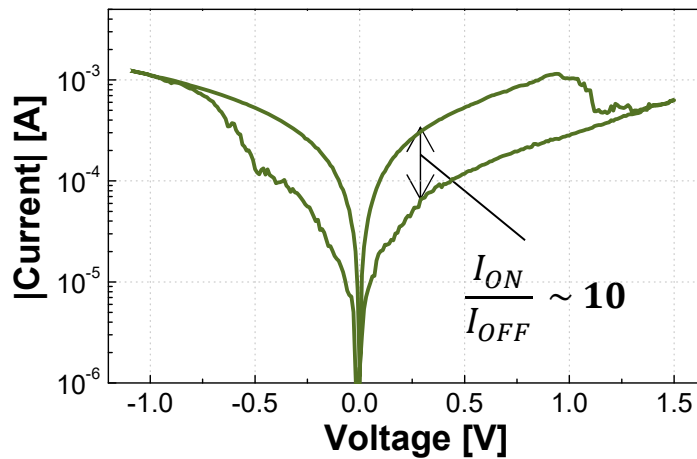
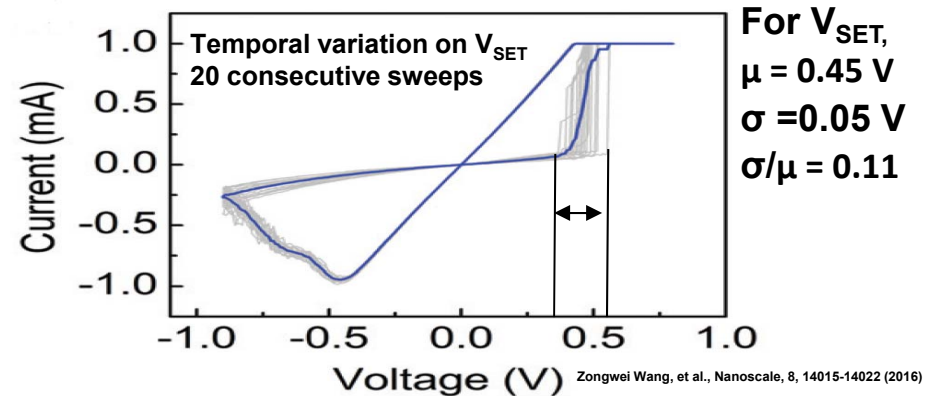
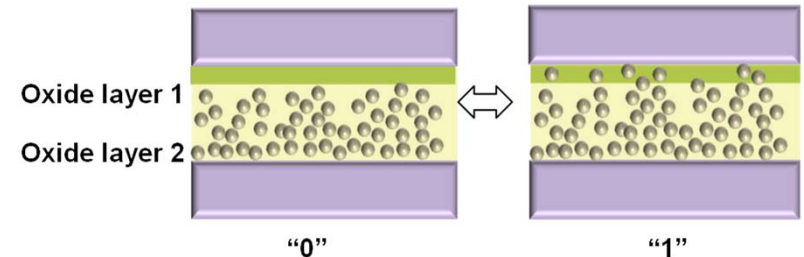
- ❑ High endurance and long retention
- ❑ High on-off ratio
- ❑ Cycle-to-cycle uniformity
- ❑ Device-to-device uniformity
- ❑ Current suppression in low voltage/reverse bias → Suppression of sneak paths
- ❑ **Linear synaptic weight update for neuromorphic (Analog)**

Type	DC on/off ratio (10^n)	Retention	Endurance (10^n cycles)	Set Voltage Spatial Variation (σ/μ)	Set Voltage Temporal Variation (σ/μ)	Reference
a-Si:Ag	4	X	X	0.03	X	4
HfO _x :Ag	10	~ ms@ RT	6	X	X	11
Al ₂ O ₃ /TiO _x	4	14 hr@ 77 °C	3	0.11	X	1
PEI/PEDOT:PSS	1	25 hr@ RT	X	X	X	9
Ta ₂ O _{5-x} /TaO _{2-x}	1	2.8 hr@ 250 °C	12	X	X	13
ZnO	1	0.3 hr@ RT	2	X	0.06	30
TiO _x	1	2.8 hr@ RT	2	0.10	X	31
SiO ₂	3	110 hr@ 85°C	X	0.10	X	32

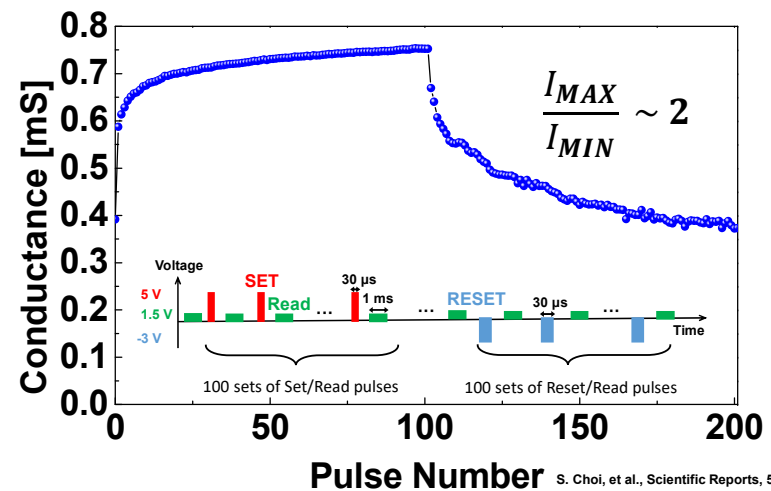
None of the currently reported RRAM fully satisfies requirements

Conventional ReRAM devices- Valence Change Memory (VCM)

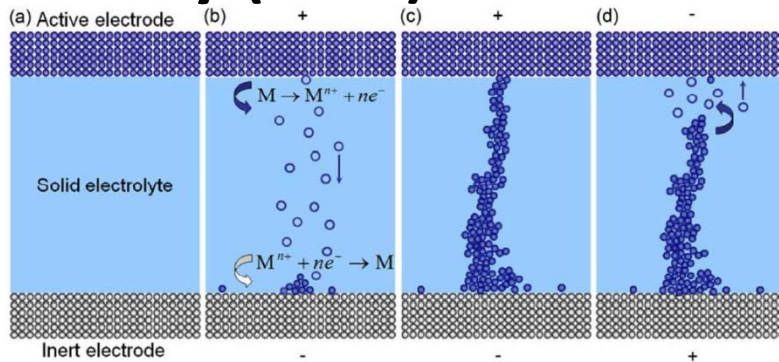
- Good endurance and retention
- **Device non-uniformity**
(Cycle-to-cycle / Device-to-device)
 - Conductive filament is not confined in single path that cause stochastic uncorrelated switching events .
- **Low On-off ratio**
 - Digital: ~ 10 / Analog: ~ 2



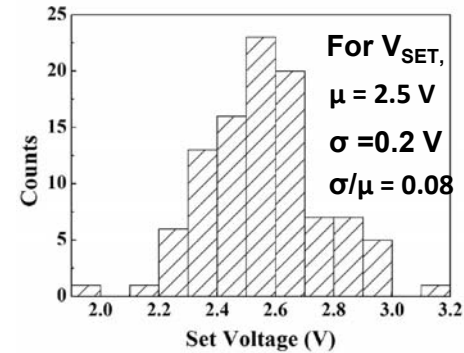
S. Kim, et al., ACS Nano, 8, 10262-10269 (2014)



Conventional ReRAM devices- Electrochemical Metallization Memory (ECM)

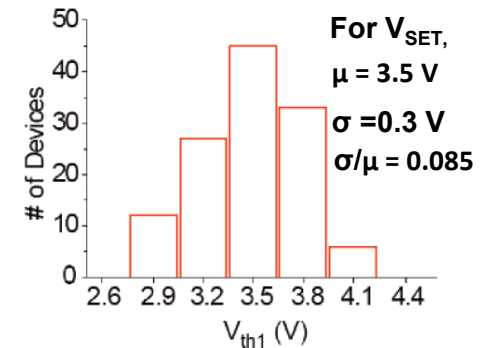


Cycle-to-cycle variation



S. Gaba, et al., IEEE EDL, 35, 2014

Device-to-device variation



S.H. Jo, et al., Nano Lett., 8, 2008

High On-off ratio

- Digital $> 10^4$ For reduced power, reduced bit-error-rate(BER) and increased read bandwidth in high density RRAM

Device non-uniformity

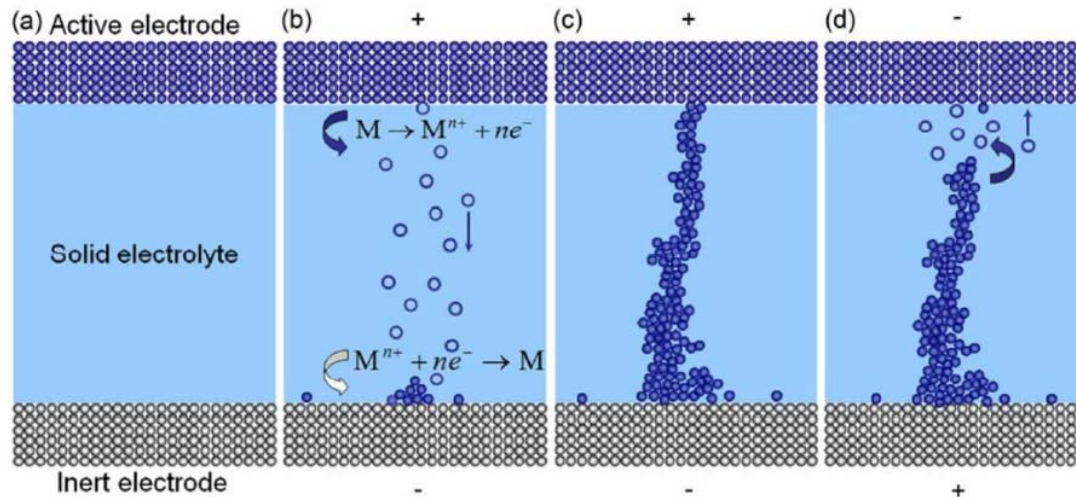
(Cycle-to-cycle / Device-to-device)

Retention/Endurance trade-off

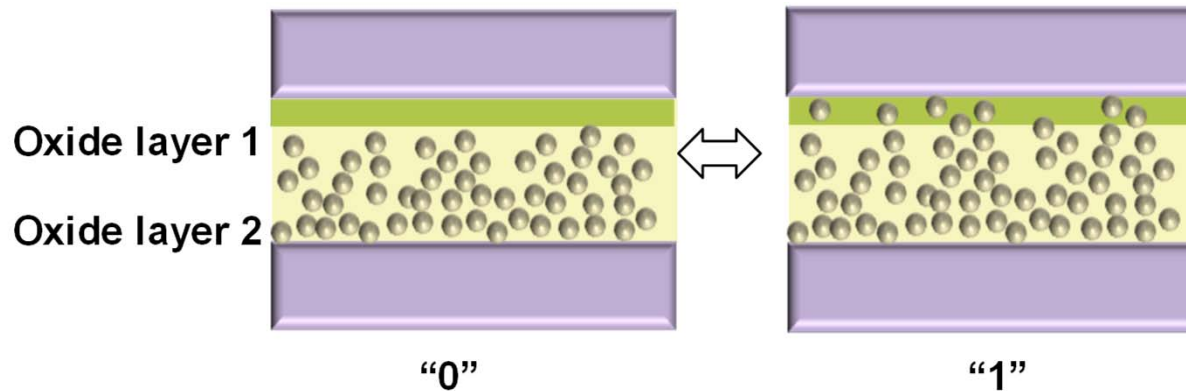
- Weak Ag-channel formation enhances endurance but reduces retention time
- Strong Ag-channel formation increases retention time but deteriorate endurance

What is the source of device variation?

- Due to amorphous switching medium



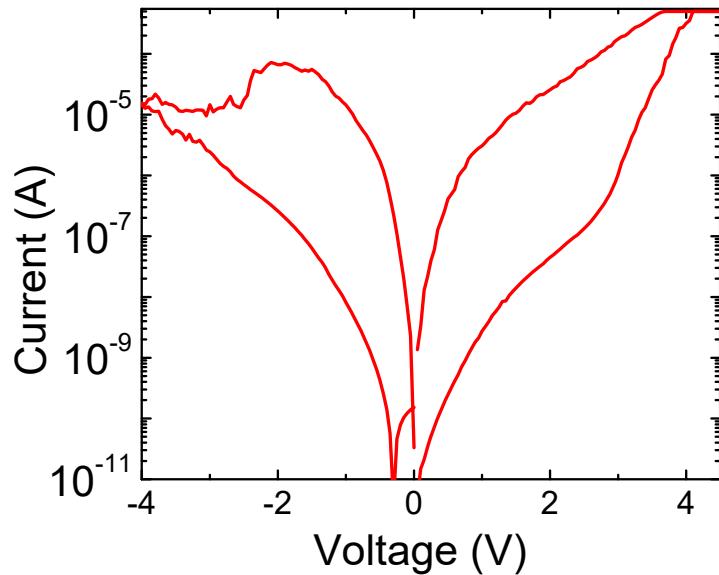
So many filament candidates



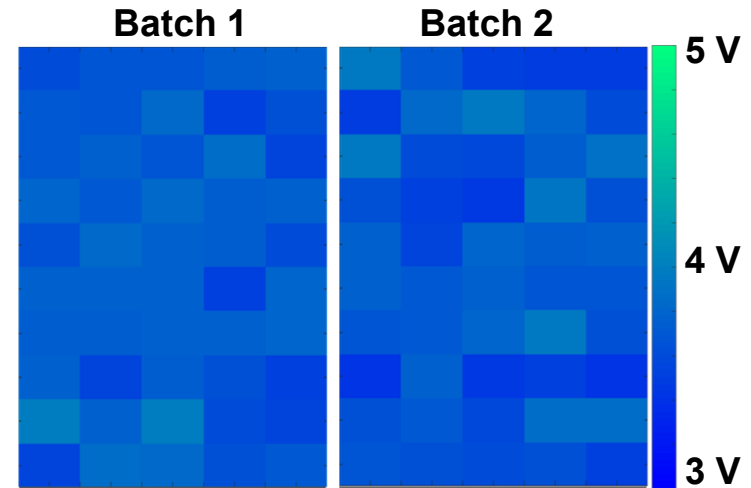
Can single-crystalline Si do resistive switching?



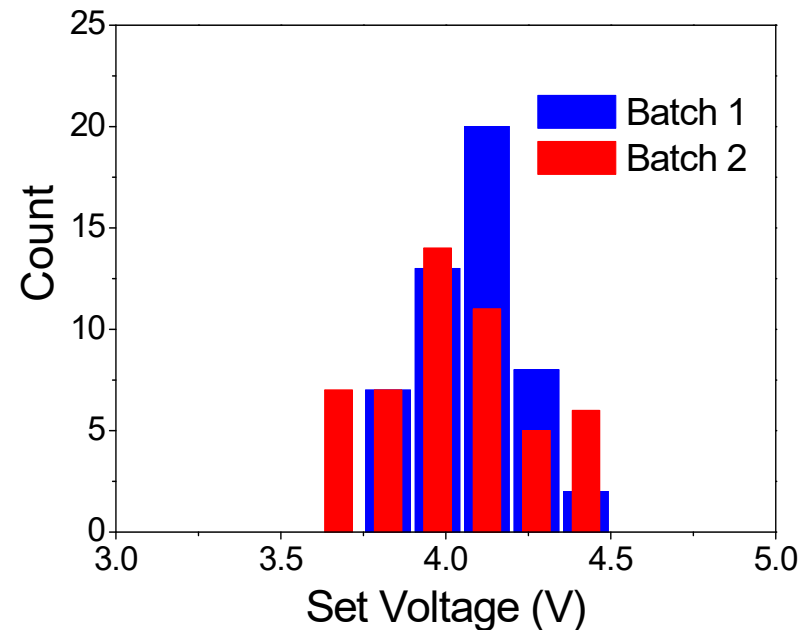
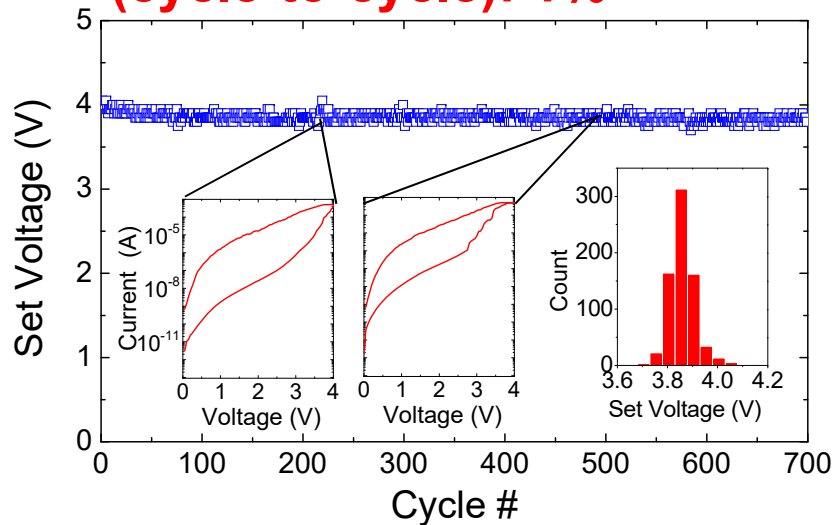
On-off ratio > 10^4



**Spatial set V variation
(device-to-device): 4%**



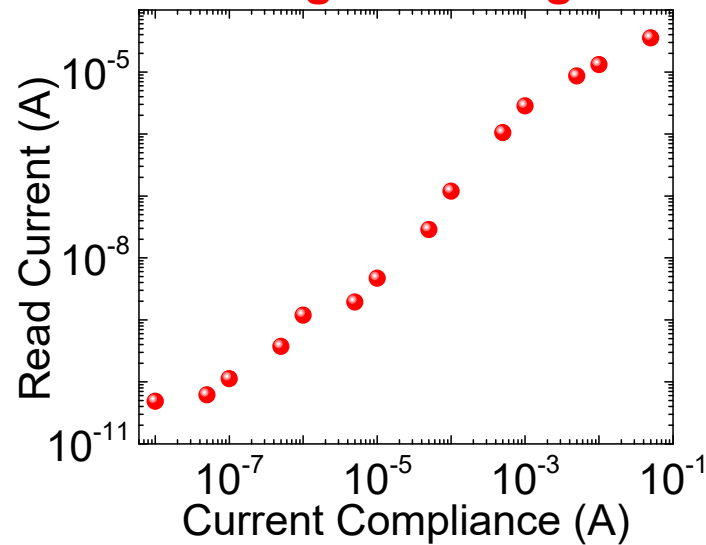
**Temporal set V variation
(cycle-to-cycle): 1%**



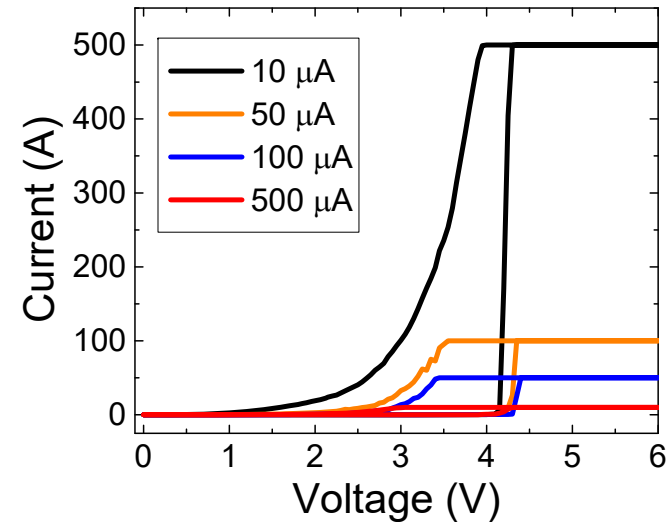
Introduction of epitaxial RAM (epiRAM) for digital



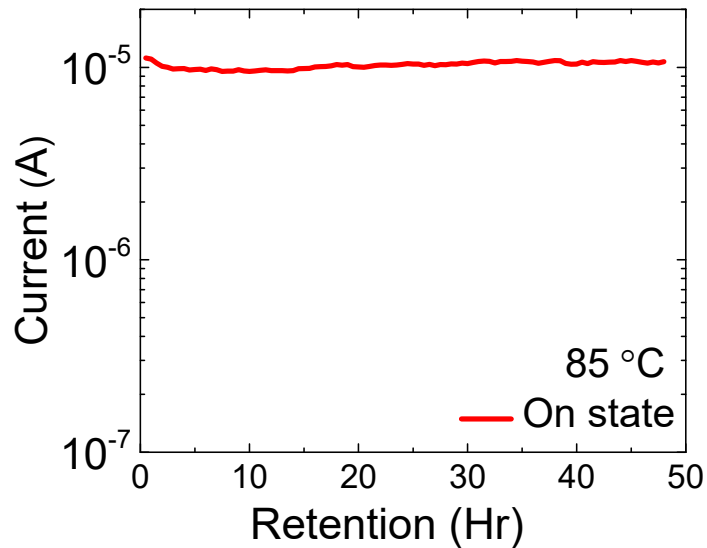
Multilevel storage from self-limiting filament growth



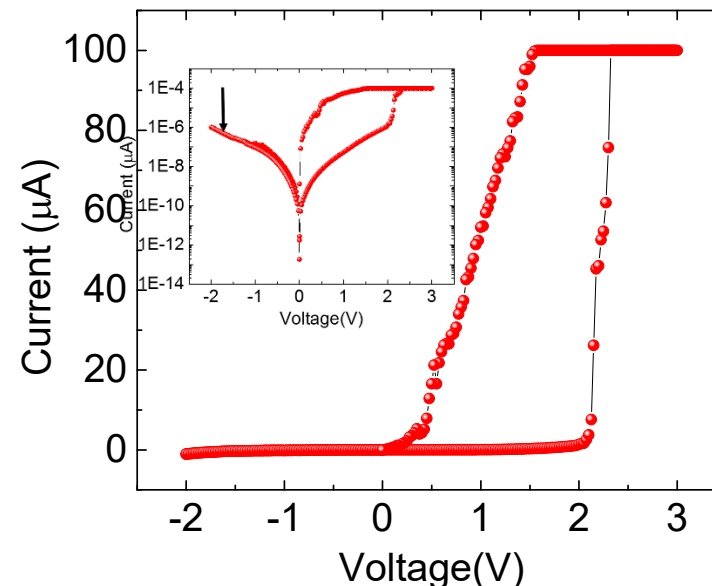
Reliable switching with different c.c



Long retention: Two days at 85°C



Self-selection



Summary of epiRAM performances



New epitaxial RAM (epiRAM) devices contains all required performances for digital and analog applications

- Long retention with long endurance
- Excellent spatial/temporal device uniformity
- High on/off ratio
 - good for both analog and digital
 - Analog: >250, Digital: 10^4
- Self-selection to reduce the impact of sneak path
- Linear weight update
- Lower power consumption

	Retention	Endurance	Retention & Endurance	On-off ratio	Uniformity	Linearity	Self-selection
VCM	Excellent	Excellent	Excellent	Low	Good	Low	N/A
ECM	Excellent	Excellent	Bad	High	Bad	Low	N/A
OURS	Excellent	Excellent	Excellent	High	Excellent	High	Excellent

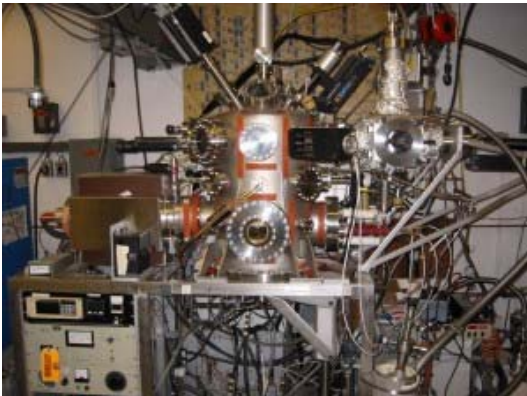
 **This will enable large-scale memory arrays for digital application as well as for neuromorphic computing**

Equipment

8" two-chamber MBE system (III-N and III-V)



2" MBE system (II-VI)



4" UHV CVD (epitaxial graphene, SiC, Si, Ge, diamond)



6" MOCVD (III-V, Si, Ge)



Collaborators

MIT:

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Harvard: Philip Kim

OSU: Jinwoo Hwang

ASU: Shimeng Yu

UIUC: Minjoo Larry Lee

IBM: Tze-Chiang Chen, Frances Ross, James Hannon, Devendra Sadana

Nanoelectronics group



Samuel Cruz (MechE) Yunjo Kim (Nano) Scott Tan (Physics) Kuan Qiao (Physics)

Graduate students

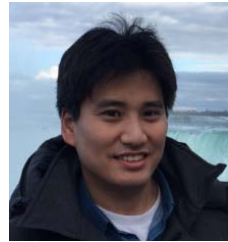


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